

EAST - [015751c:1]

File View Edit Tools Window Help

Pending

Active

- L3: (6443) quiescent near9 current
- L4: (48540) block near9 test\$4
- L6: (12582) hierarch\$4 and peripheral and memory
- L9: (167499) block same level
- L10: (63) 13 and 14 and 19
- L11: (4) 13 and 14 and 16 and 19
- L17: (10538) (quiet or quiescent or inactive) near9 current
- L18: (3) 117 and 14 and 15 and 16
- L19: (11) 117 and 14 and 16
- L20: (4) 13 and 14 and 16

Failed

Saved

- (335) quiescent near9 current near9 test\$4

USPAT:USPGPUB:1 ☒ Phrases

Default operator: OR ☒ Highlight all hit terms reliably

117 and 14 and 16

BRD GSP Image Text HTML

	U	I	Document ID	Iss	Title	Current	Ci
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030115522	20	Method and program product for designing hierarchical circuit	714/726	
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030110457	20	Method and program product for designing hierarchical circuit	716/4	
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6112298 A	20	Method for managing an instruction execution pipeline during	712/227	
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6081885 A	20	Method and apparatus for halting a processor and providing s	712/227	71
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6065106 A	20	Resuming normal execution by restoring without refetching in	712/24	71
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6055649 A	20	Processor test port with scan chains and data streaming	714/30	
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6016555 A	20	Non-intrusive software breakpoints in a processor instructio	714/35	71
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5970241 A	19	Maintaining synchronism between a processor pipeline and sub	712/227	
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5323444 A	19	Emergency call system with call capacity/last chance routing	379/45	37
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4947316 A	19	Internal bus architecture employing a simplified rapidly exe	710/105	71
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030115522	20	Integrated circuit designing method involves synchronizing q		

File Details HTML

Ready

NUM